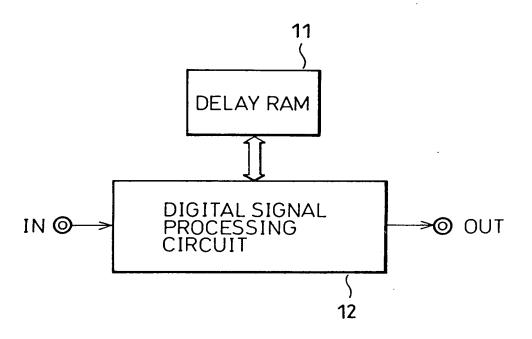
FIG.1



DELAY RAM

23

DOWN-SAMPLING
CONVERTER

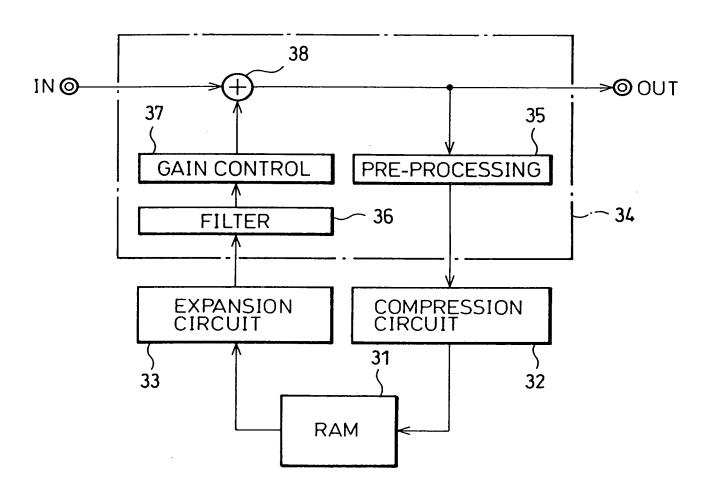
DIGITAL SIGNAL
PROCESSING
CIRCUIT

24

DOWN-SAMPLING
CONVERTER

DIGITAL SIGNAL
PROCESSING
CIRCUIT

FIG.3



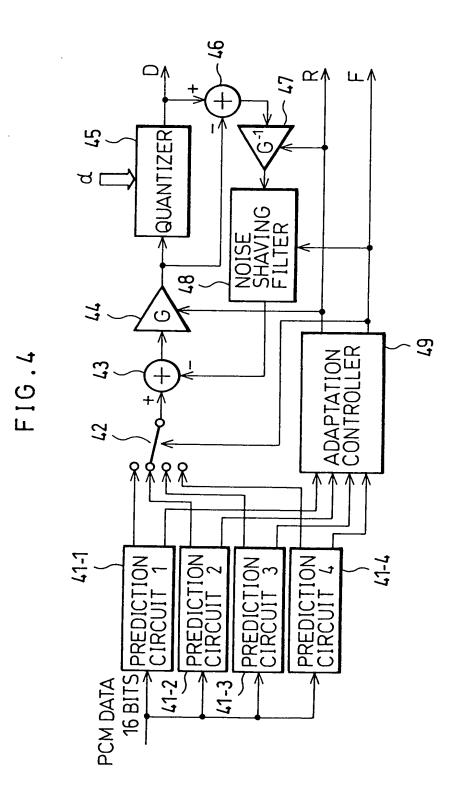
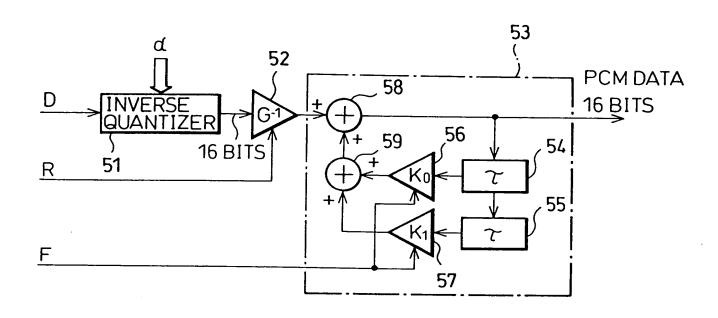


FIG.5



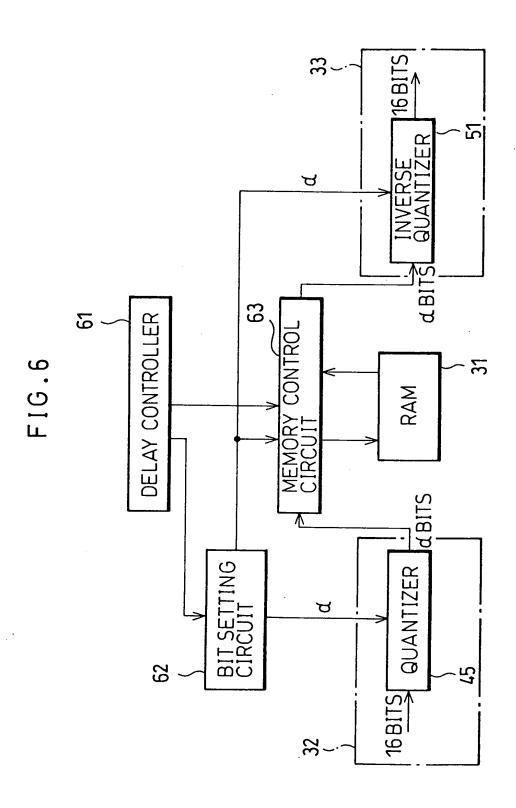


FIG.7

